

(19)



Europäisches Patentamt
European Patent Office
Office européen des brevets



(11)

EP 0 975 054 A2

(12)

EUROPEAN PATENT APPLICATION

(43) Date of publication:
26.01.2000 Bulletin 2000/04

(51) Int. Cl.⁷: H01R 12/18

(21) Application number: 99113364.6

(22) Date of filing: 10.07.1999

(84) Designated Contracting States:
AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU
MC NL PT SE
Designated Extension States:
AL LT LV MK RO SI

(30) Priority: 17.07.1998 US 120527
22.07.1998 US 121316

(71) Applicant: MOLEX INCORPORATED
Liste Illinois 60532-1682 (US)

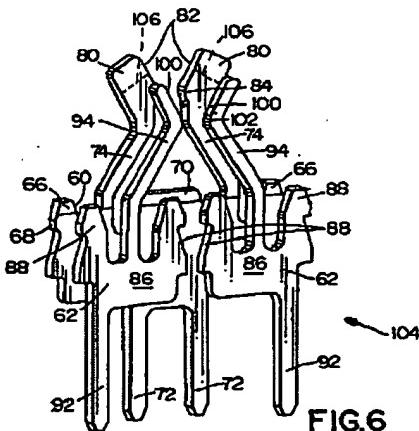
(72) Inventors:
• Tung, Shyh-Lin
Taiping City, Taichung Hsien (TW)

• Lang, Harold Keith
Fox River Grove, IL 60021 (US)
• Panella, Augusto P.
Naperville, IL 60564 (US)
• Triner, Irvin R.
Willow Springs, IL 60480 (US)
• Walse, Alan S.
LaGrange, IL 60525 (US)

(74) Representative:
Blumbach, Kramer & Partner GbR
Patentanwälte,
Alexandrastrasse 5
65187 Wiesbaden (DE)

(54) High performance card edge connector

(57) A card edge connector for mounting on a circuit board (14) and removably receiving a circuit card (16) includes an elongated housing (26) defining a card receiving slot (34). Numerous terminal receiving cavities (44, 46) intersect and extend to both sides of the slot (34). Alternate cavities include stamped reference (ground or power) terminals (60) and signal terminals (62), all having downwardly extending board contacts (72, 92) and upwardly extending spring arms (76, 78; 96, 98). There are numerous similar sets of face to face contacts, each including a reference contact parallel to and substantially overlying an opposed pair of signal contacts.



EP 0 975 054 A2

Description**Background of the Invention**

[0001] The present invention relates to electrical connectors for printed circuit boards, and more particularly to an improved card edge connector for removably connecting a circuit card to a circuit board.

Description of the Prior Art

[0002] Devices such as computers using printed circuit boards are exhibiting increasing circuit densities and operate at increasing frequencies. For example, the speeds of high frequency digital signals traveling between a computer motherboard and densely populated memory module printed circuit cards on an associated circuit board are becoming higher. These trends create problems for electrical connectors such as edge card connectors that are used to removably mount a circuit card on a circuit board.

[0003] With increasing circuit density, the electrical connectors and the electrical terminals they include are smaller. The terminals must nevertheless be sufficiently flexible and strong to provide reliable contact with a circuit card inserted into the connector.

[0004] In addition, it is desirable to keep small the impedance of the circuit paths provided by the electrical terminals of the edge card connector. Meanwhile, inductance must be kept to a minimum, capacitance must be carefully controlled, and crosstalk between different signals must be minimized. These often conflicting goals have led to many approaches for connector and terminal design with varying degrees of success.

[0005] US-A-5,161,987, for example, discloses an electrical connector having a ground bus with a plurality of solder tails. A row of signal contacts is located on each side of the ground bus.

[0006] US-A-5,162,002, meanwhile, discloses a card edge connector with spatially overlapped terminals having relatively shorter and relatively longer contact elements. This connector has important advantages such as reducing the peak card insertion force, but has electrical characteristics that are not optimized for higher speed digital signals.

[0007] US-A-5,192,220 discloses a dual readout socket wherein crosstalk is reduced by increasing the space between connectors. This approach defeats the goal of increased circuit density.

[0008] US-A-5,259,768 discloses an electrical connector having ground terminals with significantly larger surface areas than the signal terminals. The ground and signal terminals alternate, and the shadowing effect of the ground terminals reduces crosstalk. The ground terminals have both solder tails and grounding feet to reduce impedance generally, while non-functional stubs are sized to provide a specifically desired impedance.

[0009] US-A-5,259,793 discloses an edge connector

with terminals arranged in an alternating array along the circuit card insertion slot. Circuit density is diminished because of the alternating array.

[0010] 5 US-A-5,309,630 discloses an electrical connector wherein a desired impedance is obtained by selecting terminals having anchoring portions sized to correspond to the desired impedance. Signal and ground terminals may alternate, and at least the ground terminals are provided with two feet to reduce impedance.

[0011] 10 US-A-5,580,257 discloses a connector in which enlarged ground terminals are adjacent to pairs of signal terminals to reduce crosstalk. Although this arrangement has advantages, three different terminal shapes are required, and the operation of assembling terminals into the connector housing is complex.

[0012] 15 Despite these and many other attempts, there remains a long-standing need for a card edge connector that can be made at reasonable cost, is robust and reliable, has high circuit density and performs well in high speed digital circuits.

Summary of the Invention

25 [0013] An object of the present invention is to provide an improved card edge connector. Other objects are to provide a connector with low inductance that can achieve an impedance match with associated circuit assemblies; to provide a connector having minimum cross talk between signal circuits; to provide a connector having high circuit density; to provide a connector that is robust although small; to provide a mechanically and electrically reliable connector that can be manufactured and assembled inexpensively; and to provide an improved card edge connector overcoming disadvantages of connectors used in the past.

[0014] 30 In accordance with the invention there is provided a card edge connector for receiving a mating edge of a circuit card having a plurality of contact pads. The edge card connector includes an elongated insulating housing having top and bottom walls and an elongated card slot in the top wall for receiving the mating edge. A plurality of pairs of parallel, closely spaced terminal receiving cavities in the housing intersect and extend transverse to the slot. One cavity of each pair contains at least one signal terminal for conducting high frequency digital signals and the other cavity of each pair contains at least one reference terminal for conducting a ground or power signal. Each signal terminal 35 includes a signal terminal body retained in the housing and a signal terminal card contact including a signal terminal flexible beam extending upward from the body and a signal terminal contact region in the slot. Each reference terminal includes a reference terminal body retained in the housing and overlying the signal terminal body, and a reference terminal card contact including a reference terminal flexible beam extending upward from the body portion and a reference terminal contact

region in the slot.

Brief Description of the Drawings

[0015] The present invention together with the above and other objects and advantages may best be understood from the following detailed description of the preferred embodiment of the invention illustrated in the drawings, wherein:

- Fig. 1 is an isometric view of a printed circuit board assembly including card edge connectors embodying the present invention mounted on a circuit board and connecting removable circuit cards to the circuit board;
- FIG. 2 is a side elevational view of one of the card edge connectors of FIG. 1;
- FIG. 3 is an enlarged vertical sectional view of the housing of the card edge connector taken along the line 3-3 of FIG. 2 and illustrating a terminal receiving cavity prior to mounting of terminals into the housing;
- FIG. 4 is a sectional view similar to FIG. 3 illustrating a reference terminal mounted in a terminal receiving cavity;
- FIG. 5 is a view similar to FIG. 3 illustrating signal terminals mounted in a terminal receiving cavity;
- FIG. 6 is an isometric view of a reference terminal and an adjacent pair of signal terminals as they are mounted in the housing of the card edge connector, but with the connector housing removed to reveal the terminals;
- FIG. 7 is a side elevational view illustrating a pair of signal terminals in front of a reference terminal as they are mounted in the housing of the card edge connector, but with the connector housing removed to reveal the terminals;
- FIG. 8 is a side elevational, fragmentary view of a portion of a printed circuit card edge that mates with the card edge connector; and
- FIG. 9 is a fragmentary plan view of a portion of a circuit board upon which the edge card is mounted, with reference lines added to aid in the description of the invention.

Detailed Description of the Preferred Embodiments

[0016] Having reference now to the drawings, in FIG. 1 there is illustrated a circuit assembly generally designated as 10 and including three card edge connectors, each generally designated as 12, constructed in accordance with the principles of the present invention. The circuit assembly 10 includes a printed circuit board 14, for example, a computer motherboard. The card edge connectors 12 are mounted on the circuit board 14 and removably receive printed circuit cards 16, for example, memory modules with random access memory availa-

ble to the motherboard 14. The card edge connector provides circuit paths so that power, ground and digital signals can be transferred between the circuit board 14 and the circuit cards 16.

- [0017] The pertinent structure of the circuit card 16 and the circuit board 14 are shown in FIGS. 8 and 9. The card 16, of which a fragment is seen in FIG. 8, includes an edge 18 that mates with the card edge connector 12. A series of conductive contact pads 20 is provided on both opposed surfaces of the card 16 along the mating edge 18. Conductive traces on and/or within the card 16 provide power, ground and signal paths leading from the contact pads 20 to components (not shown) that are mounted on the card 16.
- [0018] A fragment of the circuit board 14 is shown in FIG. 9. The upper surface 22 of the board includes an array of conductive regions 24. In the illustrated embodiment, the conductive regions 24 are plated through holes. Other arrangements, such as conductive pads for surface mount soldering connections, are also possible. Circuit traces on and/or in the circuit board 14 provide power, ground and signal paths from the conductive regions 24 to other components (not shown) mounted on the circuit board. When the card edge connector 12 is mounted on the circuit board 14 and when a circuit card 16 is inserted into the card edge connector 12, the connector 12 provides circuit paths between the contact pads 20 and the conductive regions 22.

[0019] As seen in FIGS. 2 and 3, the card edge connector 12 includes an elongated housing 26 made of an electrically insulating material such as a molded high temperature thermoplastic, such as liquid crystal polymer plastic. The housing has a top wall 28, a bottom wall 30 and opposed side walls 32. An elongated card slot 34 in the top wall 28 receives the mating edge 18 of an inserted card 16. Housing end posts 36 and latches 38 may be provided at the ends of the housing 26. The bottom wall 30 includes stand off projections 40 for maintaining a space between the bottom wall 30 and the top surface 22 (FIG. 1) of the circuit board 14. Hold downs 42 are generally known in the art and may be used to mechanically attach the housing 26 to the circuit board 14.

[0020] Numerous terminal receiving cavities 44 (FIGS. 3 and 4) and 46 (FIG. 5) are provided in the housing 26. In the preferred embodiment of the invention, there may be over fifty cavities 44 and a similar number of cavities 46. Every cavity 44 is immediately adjacent to a cavity 46, and in the preferred embodiment of the invention, the cavities 44 and 46 alternate in position along the length of the housing 26.

[0021] The cavities 44 and 46 are separated by internal separator walls or dividers 48 and extend transversely, perpendicular to the slot 34, between the side walls 32. The cavities 44 and 46 intersect and extend to both opposed sides of the slot 34. The bottom of the slot 34 has a stop surface 50 defined in part by the separator walls 48 and by terminal stop projections 52, spacers

54 in the cavities 44 and terminal retention walls 56 in the cavities 46. The separator walls 48 are connected across cavities 44 by spacer 54 that extends only slightly downward from the stop surface 50 of slot 34. On the other hand, separator walls 48 are connected across cavities 46 by terminal retention walls 5 that extend downward from the stop surface 50 of slot 34 substantially to the bottom of the housing 26. The side walls of the slot 34 are defined by the inner edges of comb-like upper portions 48a of the separator walls 48. The lower portions of the cavities 44 and 46 have opposed internal side walls 58. Each cavity 44 and 46 has an open bottom through which terminals may be inserted into the cavities.

[0022] Reference terminals 60 are mounted in the cavities 44. The term "reference terminal" is defined here to mean a terminal that provides ground or power connections between the circuit board 14 and the circuit card 16. Signal terminals 62 are mounted in the cavities 46. The term "signal terminal" is defined here to mean a terminal that provides a circuit path for the transmission of ac signals, typically high speed digital signals, between the circuit board 14 and the circuit card 16.

[0023] In the preferred embodiment, the reference terminals 60 are all identical to one another and the signal terminals 62 are all identical to one another. The terminals 60 and 62 are flat, planar bodies of metal of uniform thickness, preferably made by stamping from metal sheet stock without any other forming or bending operations. This provides a more efficient manufacturing operation and a sturdier and more reliable terminal in comparison with electrical connectors having terminals that are both stamped and formed. Preferably the terminals 60 and 62 are stamped of phosphor bronze and plated with tin and lead over nickel, with selective gold plating at electrical contact areas, though other alloys or conductive materials may be used.

[0024] In FIG. 4, one of the reference terminals 60 is seen in place in one of the cavities 44. The terminal 60 includes a generally rectangular, planar, plate like body 64 having upwardly extending retention arms 66 at both ends. The arms 66 have barbs 68 that engage the internal side walls 58 and resist removal of the terminal 60 after the terminal 60 is loaded into the cavity 44 through the bottom wall 30. Terminal insertion may be limited by engagement of the arms 66 against the projections 52 or by engagement of a central span portion 70 of the body 64 against the spacer 54. The reference terminal 60 extends across the full width of the cavity 44 and extends to both sides of the slot 34.

[0025] A pair of spaced apart board contacts 72 extend downward from the body 64. These contacts are received in the plated through conductive regions 24 of the circuit board 14 to connect the terminal 60 to the circuit board. The conductive regions 24 connected to the reference terminals 60 are at a reference voltage of ground or power supply potential. It is important that the connections made to reference voltage be of low imped-

ance. The use of two spaced board contacts for the single reference terminal 60 results in parallel redundant circuit paths and low inductance.

[0026] A pair of opposed spring arms 74 extend upward from the body 64. Each spring arm 74 includes a flexible beam with a vertical portion 76 and an inwardly sloped portion 78. The end of the spring arm 74 includes a large segment 80 defining a lead-in surface 82 and a contact region 84. When the mating edge 18 of the circuit card 16 is inserted into the slot 34, an opposed pair of conductive pads 20 enter into each of the cavities 44. The mating edge 18 engages the opposed lead in surfaces 82 and the spring arms 74 resiliently deflect or separate. When the card 16 is fully inserted, the contact regions 84 engage the pads 20 to complete circuit paths from the terminal 60 to the opposed pair of pads 20. As such, redundant paths are provided between the circuit board 14 and the circuit card 16.

[0027] Referring now to FIG. 5, a spaced apart pair of the signal terminals 62 are mounted in each of the cavities 46. The use of pairs of discrete signal terminals 62 rather than a single terminal such as reference terminal 60 permits a high circuit density. Each signal terminal 62 includes a generally rectangular, planar, plate like body 86 having upwardly extending retention arms 88 at both ends. The arms 88 have barbs 90 that retain the terminals 62 in the cavity 46. At the outer ends of the bodies 86, the arms 88 and barbs 90 engage the internal side walls 58. At the inner ends of the bodies 86, the arms 88 and barbs 90 engage opposite sides of the retention wall 56. Terminal insertion is limited by engagement of the arm 66 against the projection 52.

[0028] A board contact 92 extends downward from the body 86 of each of the terminals 62 in the cavity 46. These contacts 92 are received in the plated through conductive regions 24 of the circuit board 14 to connect the terminals 62 to the circuit board 14. The conductive regions 24 connected to the signal terminals 62 are used to communicate ac signals such as high frequency digital signals between the circuit board 14 and the circuit card 16. The board contacts 92 are transversely offset from the reference terminal board contacts 72 in a staggered pattern.

[0029] A spring arm 94 extends upward from each of the bodies 86. Each spring arm 94 includes a flexible beam with a vertical portion 96 and an inwardly sloped portion 98. The end of the spring arm 94 includes a lead-in surface 100 and a contact region 102. The two identical signal terminals 62 are loaded into opposite sides of the cavity 46 in reversed positions relative to one another. The two terminals 62 are at opposite sides of the slot 34, and because of the reverse orientation, the two opposed spring arms 94 slope toward one another at opposite sides of the slot 34.

[0030] When the mating edge 18 of the circuit card 16 is inserted into the slot 34, an opposed pair of conductive pads 20 enter into each of the cavities 46. The mat-

ing edge 18 engages the opposed lead-in surfaces 100 and the spring arms 94 resiliently deflect or separate. When the card 16 is fully inserted, the contact regions 102 engage the pads 20 to complete circuit paths from the terminals 62 to the opposed pair of pads 20. The use of two distinct terminals 62 in each cavity 46 permits independent signal connections to be made to the opposed contact pads 20 at opposite sides of the circuit card 16.

[0031] Because every signal terminal cavity 46 is immediately adjacent to one of the reference terminal cavities 44, the connector 12 of the present invention includes numerous terminal sets generally designated as 104, each including closely spaced and interfacing reference and signal terminals 60 and 62. One of these many terminal sets 104 is shown in FIGS. 6 and 7 with the housing 26 omitted to reveal more of the structure of the terminal set. In the preferred embodiment of the invention, each set 104 includes a single reference terminal 60 and an opposed pair of signal terminals 62 but principles of the invention can apply to other arrangements including two reference and two signal terminals or one reference and one signal terminal in each set. In the preferred embodiment, the reference terminal cavities 44 alternate with the signal terminal cavities 46, but there could be other configurations such as two adjacent signal terminal cavities 44 between each pair of reference terminal cavities.

[0032] As seen in FIGS. 6 and 7, in each terminal set 104 the reference terminal 60 is parallel to and close to the pair of signal terminals 62. The reference terminal 60 substantially entirely overlies or shadows the signal terminals 62. The reference terminal body 64 entirely overlies the signal terminal bodies 86. The reference terminal body is enlarged beyond the extent of the signal terminal bodies 86 by the provision of the central span portion 70 and by downwardly extending the body 64 at the bases of the board contacts 72 as seen in FIG. 7. The signal terminal inner retention arms 88 are overlaid by the retention arms 66 and by the span portion 70. The signal terminal contact beams 74 are overlaid by the reference terminal contact beams 94 except for the small contact regions 102. This construction maximizes coupling of the signal terminals 62 to the reference terminal 60 and minimizes crosstalk between signal paths. The relatively massive structure of the reference terminal 60 reduces inductive impedance.

[0033] The enlarged segments 80 of the reference terminal contact arms 74 provide a large surface area overlying the ends of the signal terminal contact arms 94. Because these segments are larger than required for the conventional mechanical and electrical functions of the contact arms 74, they are defined as "oversize". The oversize segments 80 provide several important functions. They increase coupling to the signal terminals 62 without significantly adding mass to functional parts of the terminal and possibly impeding mechanical operation. They provide a sturdy and rugged card lead-

in area. The use of numerous such reference terminals 60 all having oversize segments in a symmetrical array at both sides of the circuit card 16 provides increased electrostatic shielding of circuits on both sides of the circuit card 16.

[0034] Another advantage of the oversize segments 80 is that the size of the segments 80 can be changed to adjust terminal impedance without interfering with the operation of the terminal. The segments could be reduced in length by having them extend only to the broken lines designated as 106 in FIG. 6. The resulting terminal would have an impedance different from a terminal as illustrated with larger segments 80. Though other sections of the terminal may need to be correspondingly resized, this feature permits the terminal to be tailored or tuned to specific impedance requirements without interfering with the mechanical function of the terminal.

[0035] As can best be seen in FIG. 7, the reference terminal contact regions 102 are at a higher elevation than the signal terminal contact regions 84. When the mating edge 18 of the circuit card 16 is inserted into the slot 34, it first contacts the reference terminal contact arms 74 and reacts against the lead-in surfaces 82 to resiliently deflect or separate the arms 74. Thereafter, the mating card edge 18 contacts the signal terminal lead-in surfaces 100 and deflects or separates the signal terminal contact arms 94. Peak insertion forces are reduced by separating these two contact engagement actions.

[0036] The card edge connector 12 of the present invention provides an advantageous array of circuit paths between the circuit board 14 and the terminals 60 and 62. FIG. 9 illustrates a fragmentary portion of the circuit board 14 showing the array of plated through hole conductive regions 24 through which extend board contacts 72 and 92. A reference line 108 identifies the longitudinal centerline of the array, coinciding with the longitudinal centerline of the slot 34 and the center of the inserted circuit card 16. An important feature of the circuit path array is that all the circuits are symmetrical about this centerline 108.

[0037] The conductive regions 24 and the board contacts 72 and 92 inserted therein are located in four lines all parallel to the centerline 108, two inner lines 110 and 112 and two outer lines 114 and 116. The inner lines 110 and 112 are closer to the centerline 108 than are the outer lines 114 and 116. In the preferred embodiment, the lines 110, 112, 114 and 116 are equally spaced, but if desired the lines 110 and 112 could be spaced farther apart while maintaining symmetry around the centerline 108.

[0038] The inner lines 110 and 112 of conductive regions 24 receive only the contacts of a single type of terminal and the outer lines 114 and 116 receive only the contacts of the other type of terminal. In the illustrated arrangement, the inner lines 110 and 112 of through holes 24 receive only the reference terminal

board contacts 72 and the outer lines 114 and 116 receive only signal terminal board contacts 92.

[0039] Every reference terminal board contact 72 is transversely aligned with and spaced an equal distance from the centerline 108 as another reference terminal contact 72. A transverse line 118 intersects two contacts 72 and illustrates this relationship. Every signal terminal contact 92 also is transversely aligned with and spaced an equal distance from the centerline 108 as another signal terminal contact 92. Another transverse line 120 intersects two contacts 92 and illustrates this relationship.

[0040] The circuit path array resulting from the present invention can facilitate routing of conductive traces on the circuit board 14 in comparison with conventional asymmetrical circuit arrays. In addition, the symmetrical array is a characteristic of a terminal pattern that facilitates connector manufacture and assembly.

[0041] In the preferred embodiment of the invention, the board contacts 72 and 92 are solder tails that are soldered to the plated through holes 24 of the circuit board 14. Other types of board contacts such as surface mount feet may be used, while retaining the advantages of the present invention. In addition, for some applications, it may be possible to utilize only one board contact for the reference terminals 60.

[0042] While the present invention has been described with reference to the details of the embodiment of the invention shown in the drawing, these details are not intended to limit the scope of the invention as claimed in the appended claims.

Claims

1. A card edge connector for receiving a mating edge of a circuit card (16) having a plurality of contact pads and for mounting upon a circuit board (14) having a plurality of conductive regions (24), said card edge connector comprising:

an elongated insulating housing (26) having an elongated card slot (34) for receiving said mating edge (18);

a plurality of pairs of closely spaced terminal receiving cavities (44, 46) in said housing (26), said cavities intersecting with and extending transversely to said slot (34), said cavities (60, 62) being defined by separator walls (48) extending between side walls (32) of said housing (26);

one cavity (46) of one of said pairs containing a mirrored pair of signal terminals (62) for conducting signals, one signal terminal (62) being positioned on each side of said slot (34) and facing oppositely to the other, and the other cavity (44) of said one of said pairs containing a generally planar, plate-like reference terminal (60), said reference terminal (60) extending

across substantially the entire width of said other cavity (44);

each of said signal terminals (62) including a signal terminal body (86), a signal terminal spring arm (94) having a signal terminal flexible cantilevered beam (96, 98) extending from said body (86) and a signal terminal contact region (102) disposed in said slot (34) for contacting one of said contact pads on said circuit card (16), and a board contact (92) for contacting one of said conductive regions of said circuit board (14);

said reference terminal (60) including a reference terminal body (64), two spaced apart reference terminal spring arms (74) each having a reference terminal flexible cantilevered beam (76, 78) extending from said body (64) and a reference terminal contact region (84) disposed in said slot (34) for contacting one of said contact pads on said circuit card (16), and a board contact (72) for contacting one of said conductive regions of said circuit board (14), said reference terminal spring arms being disposed within said other cavity (44), but on opposite sides of said slot (34).

2. The card edge connector of claim 1 wherein said two reference terminal flexible beams (76, 78) overlie said two signal terminal flexible beams (96, 98).
3. The card edge connector of claim 1 wherein each terminal (60, 62) includes a pair of retention barbs (68, 90) at opposite edges of said terminals for press-fitting and retaining said terminals into their respective terminal receiving cavities (44, 46).
4. The card edge connector of claim 1 wherein each said one terminal receiving cavity (46) has a center wall (56) extending generally from said card slot (34) to generally adjacent a bottom surface (50) of said housing (26) and said other cavity (44) includes a stop wall (54) immediately adjacent and below said card slot (34).
5. The card edge connector of claim 1 wherein said pair of signal terminals (62) in said one cavity (46) are identical and mounted in reverse orientations.
6. The card edge connector of claim 1 wherein at least one cavity of each pair contains a single reference terminal (60) that spans said slot (34) and extends in both opposed directions from said slot.
7. The card edge connector of claim 6

wherein said reference terminal body (64) overlies both said signal terminal bodies (86) in said one cavity (46) and each of said reference terminal flexible beams (76, 78) overlies one of said signal terminal flexible beams (96, 98) of said pair of signal terminals (62) in said one cavity (46).
6

8. The card edge connector of claim 1
wherein each of said conductive regions is a conductively plated through-hole (24) and each of said board contacts (72, 92) is a solder tail dimensioned for being inserted into one of said conductively plated through-holes.
10

15

20

25

30

35

40

45

50

55

7

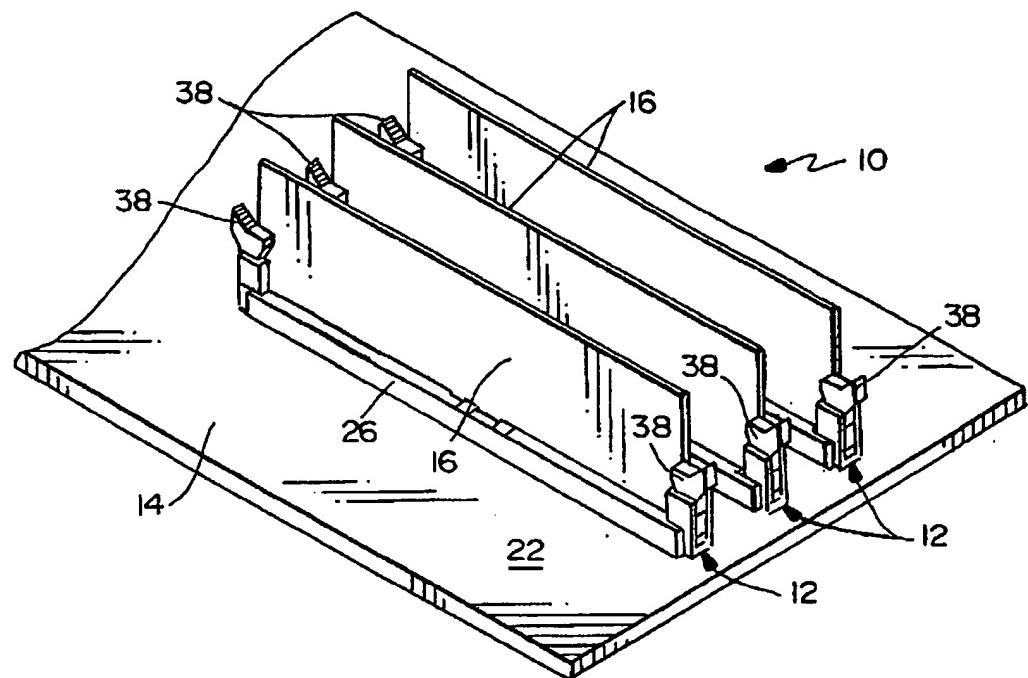


FIG.1

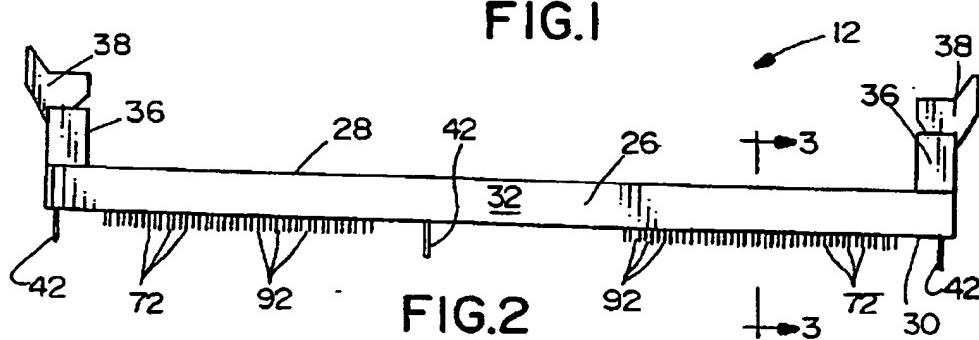


FIG.2

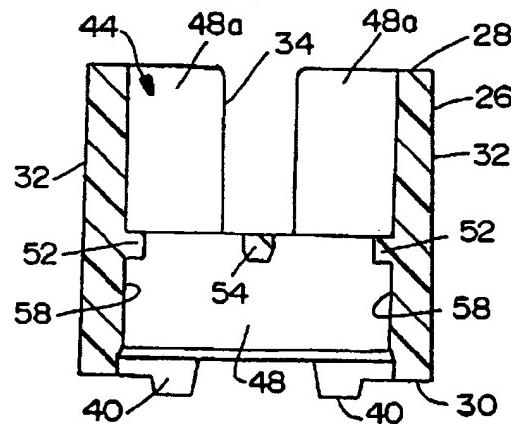


FIG.3

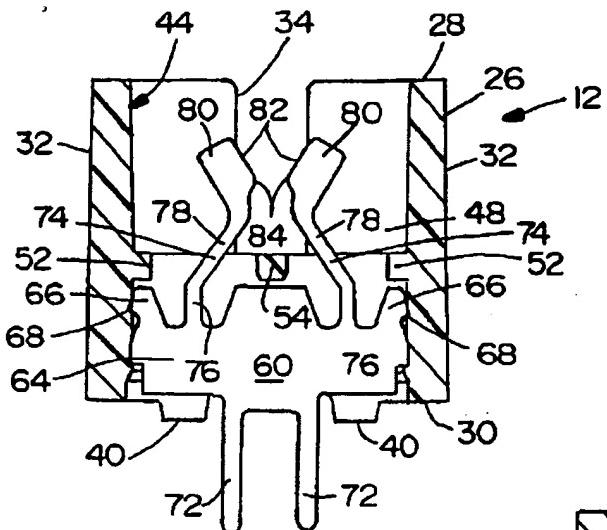


FIG.4

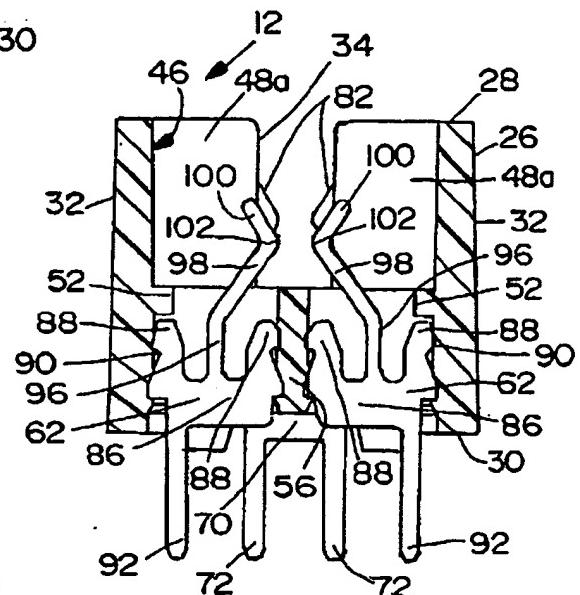


FIG.5

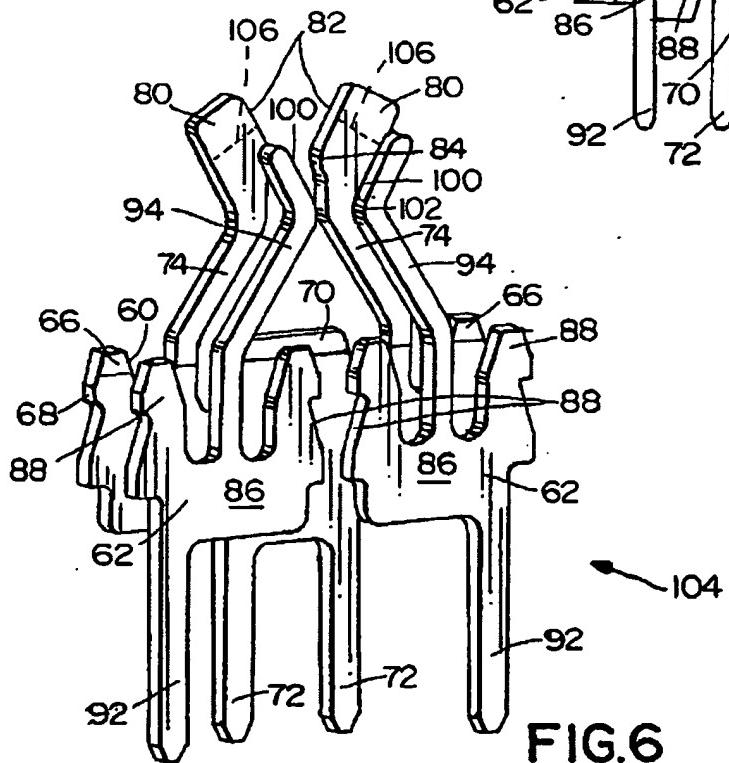


FIG.6

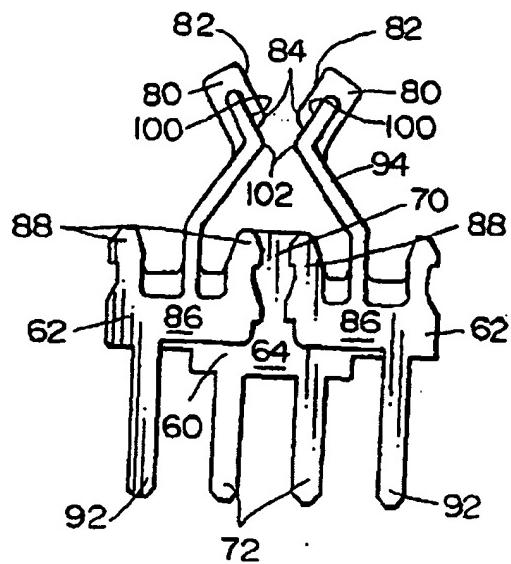


FIG.7

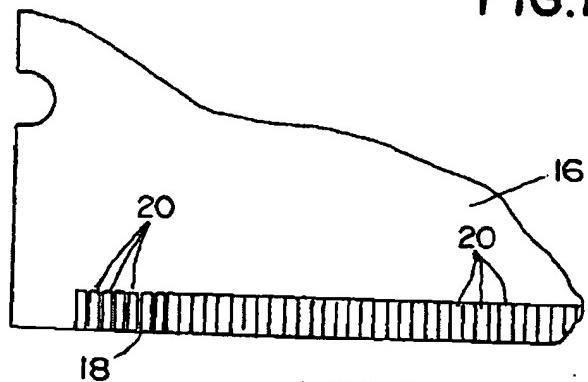


FIG.8

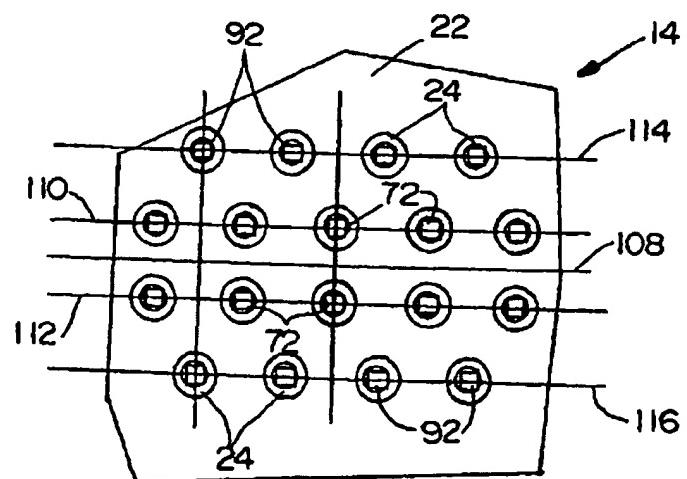


FIG.9


 European Patent
Office

EUROPEAN SEARCH REPORT

Application Number
EP 99 11 3364

DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.)
Y	EP 0 436 943 A (AMP INC) 17 July 1991 (1991-07-17) * abstract; figures 1,18 * * column 5, line 32 - column 7, line 19 * * column 12, line 48 - line 57 *	1,3,5,8	H01R12/18
Y	US 5 730 609 A (HARWATH FRANK A) 24 March 1998 (1998-03-24) * abstract; figures 1-4 * * column 2, line 53 - column 6, line 28 *	1,3,5,8	
A	"STAMPED CONNECTOR FOR I/O CARD SYSTEM" IBM TECHNICAL DISCLOSURE BULLETIN, US, IBM CORP. NEW YORK, vol. 32, no. 7, 1 December 1989 (1989-12-01), pages 200-201, XP0000078069 ISSN: 0018-8689 * figures * * page 201-202 *	2	
A	US 5 238 414 A (YAEGASHI HIROKATSU ET AL) 24 August 1993 (1993-08-24) * abstract; figures 1,4,5 * * column 2, line 24 - column 3, line 42 *	1	TECHNICAL FIELDS SEARCHED (Int.Cl.) H01R
A	GB 1 170 256 A (PAINTON & COMPANY LIMITED) 12 November 1969 (1969-11-12) * figures 1-3 * * page 2, line 94 - page 3, line 122 *	1,2	
The present search report has been drawn up for all claims			
Place of search		Date of completion of the search	Examiner
THE HAGUE		26 July 2001	Serrano Funcia, J
CATEGORY OF CITED DOCUMENTS			
X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document			
T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons A : member of the same patent family, corresponding document			

**ANNEX TO THE EUROPEAN SEARCH REPORT
ON EUROPEAN PATENT APPLICATION NO.**

EP 99 11 3364

This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report. The members are as contained in the European Patent Office EDP file on The European Patent Office is in no way liable for these particulars which are merely given for the purpose of information.

26-07-2001

Patent document cited in search report		Publication date		Patent family member(s)	Publication date
EP 0436943	A	17-07-1991		US 5026292 A US 5051099 A DE 69027012 D DE 69027012 T JP 2709364 B JP 4272676 A KR 148608 B	25-06-1991 24-09-1991 20-06-1996 02-10-1996 04-02-1998 29-09-1992 16-11-1998
US 5730609	A	24-03-1998		US 5580257 A CA 2172219 A CN 1138228 A EP 0740373 A JP 8306446 A KR 244435 B	03-12-1996 29-10-1996 18-12-1996 30-10-1996 22-11-1996 01-02-2000
US 5238414	A	24-08-1993		JP 2583839 B JP 5029041 A	19-02-1997 05-02-1993
GB 1170256	A	12-11-1969		NONE	